

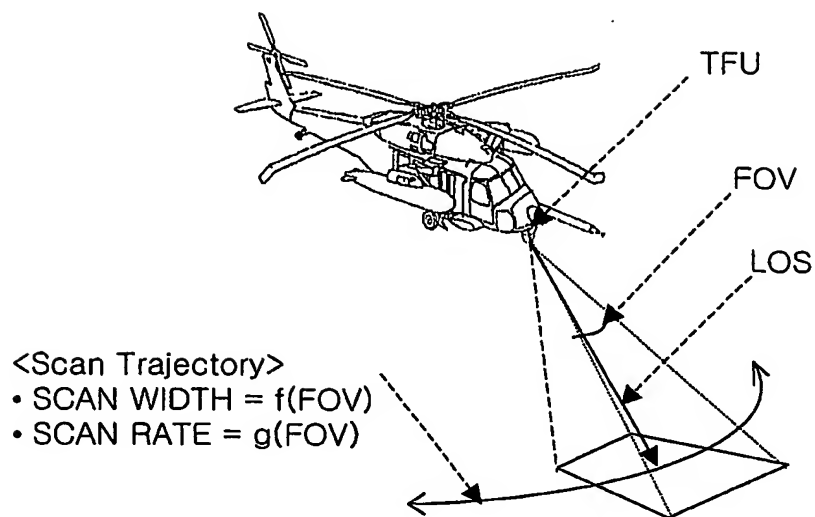
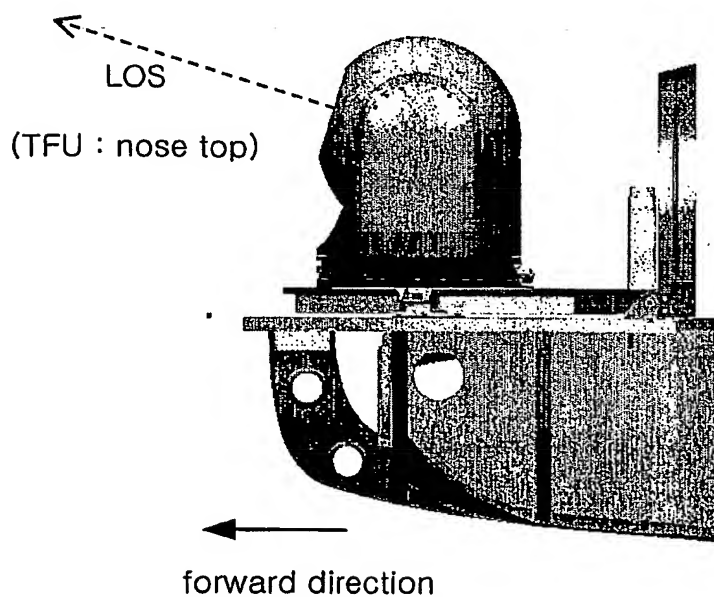
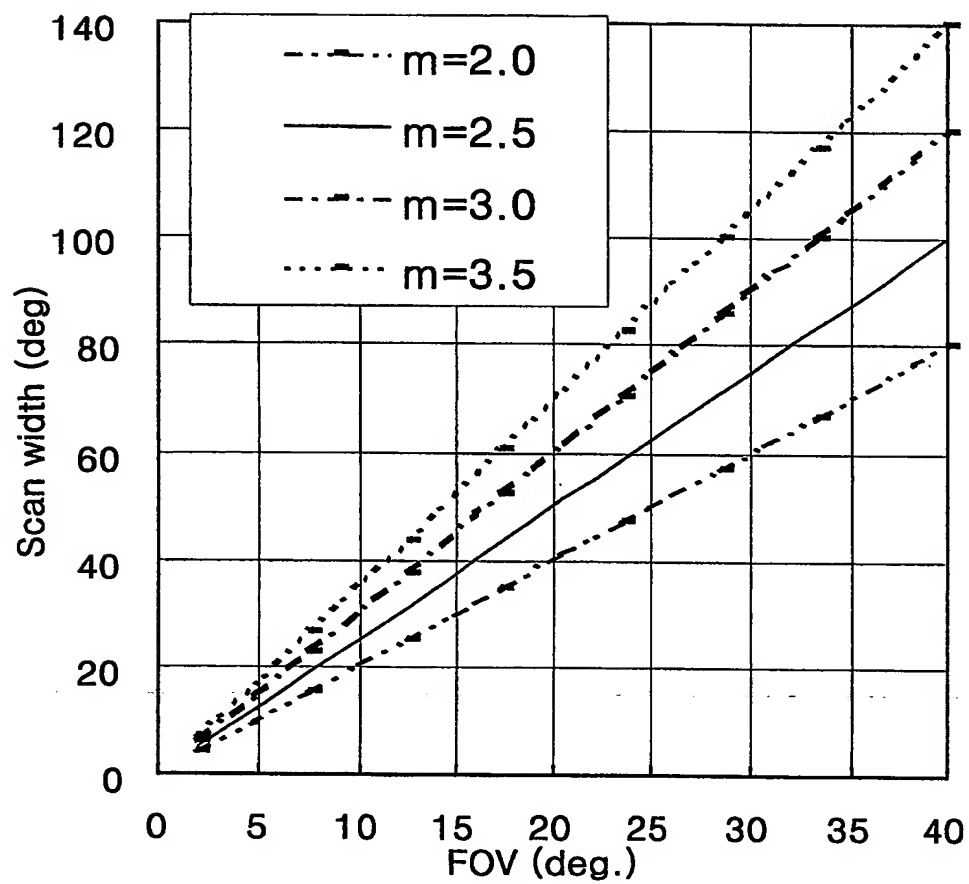
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Fig. 1A

Fig. 1B



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Fig. 2

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Fig. 3B

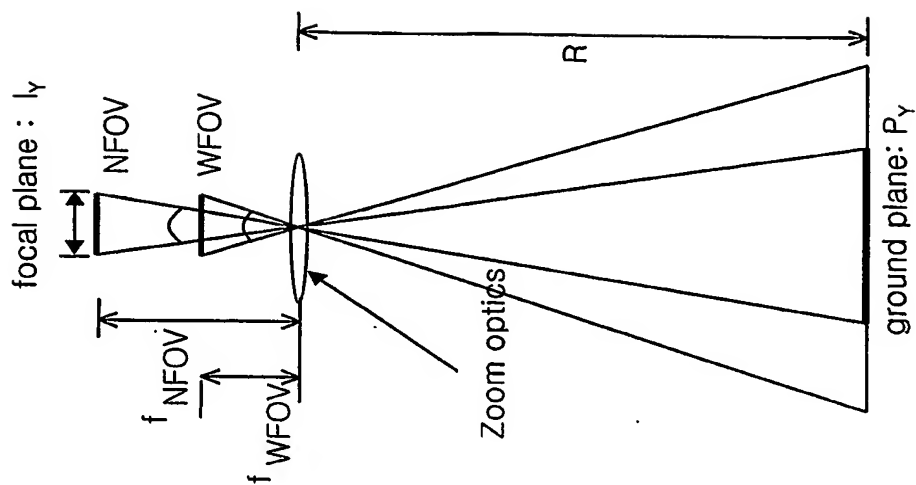
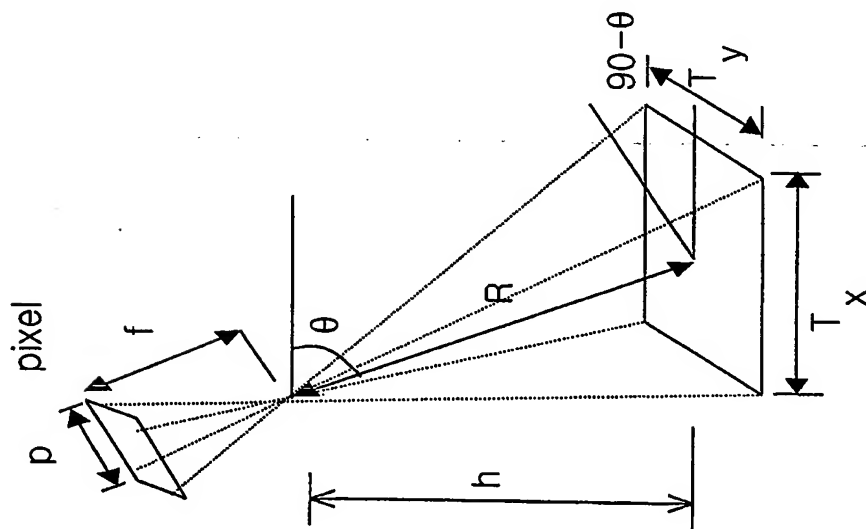
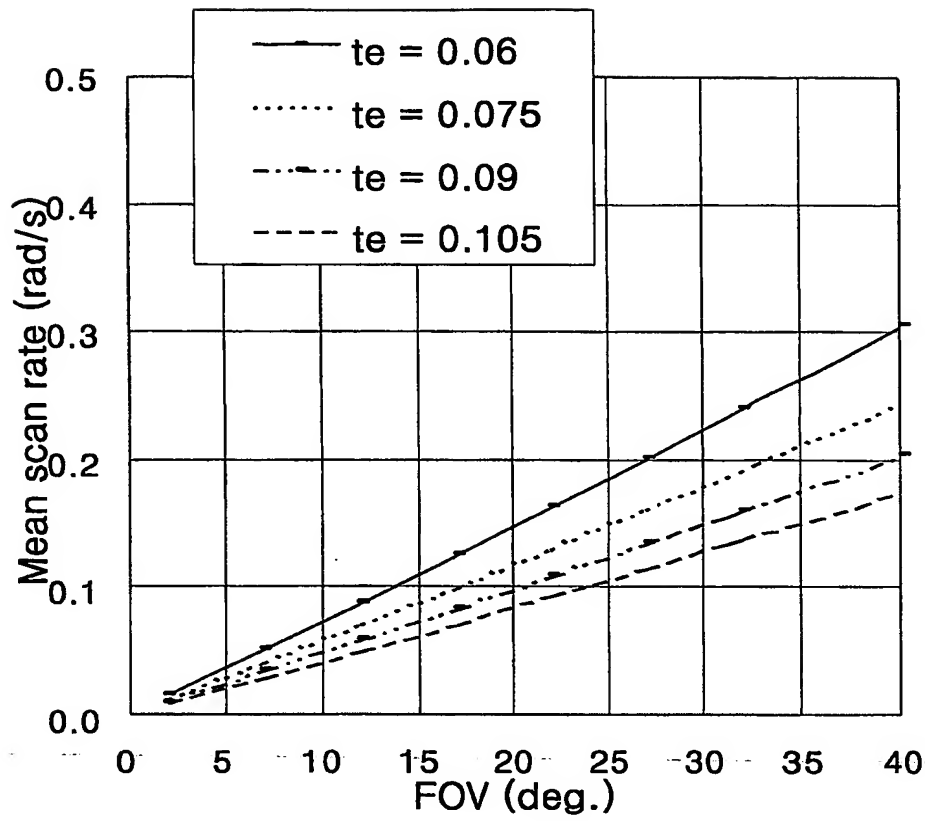


Fig. 3A



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Fig. 4

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Fig. 5

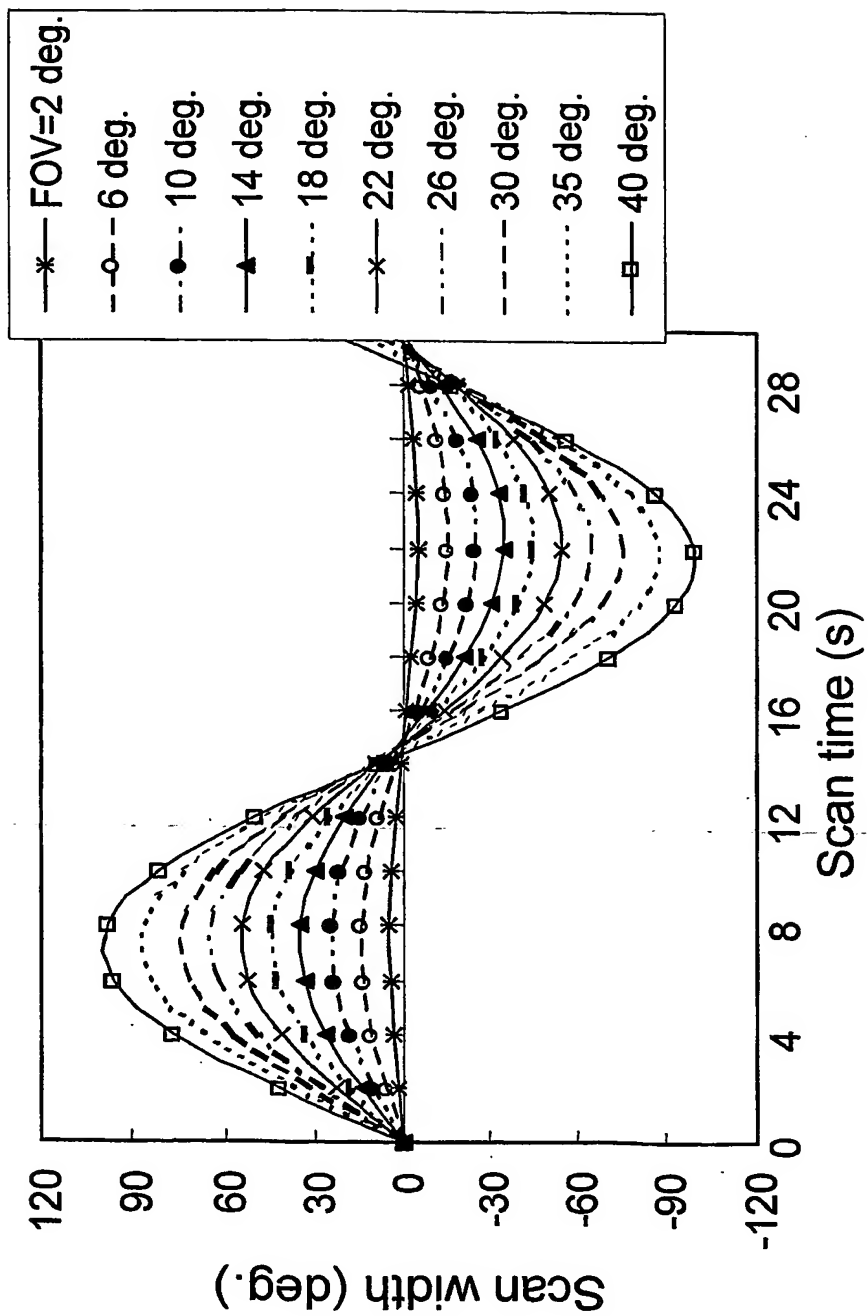
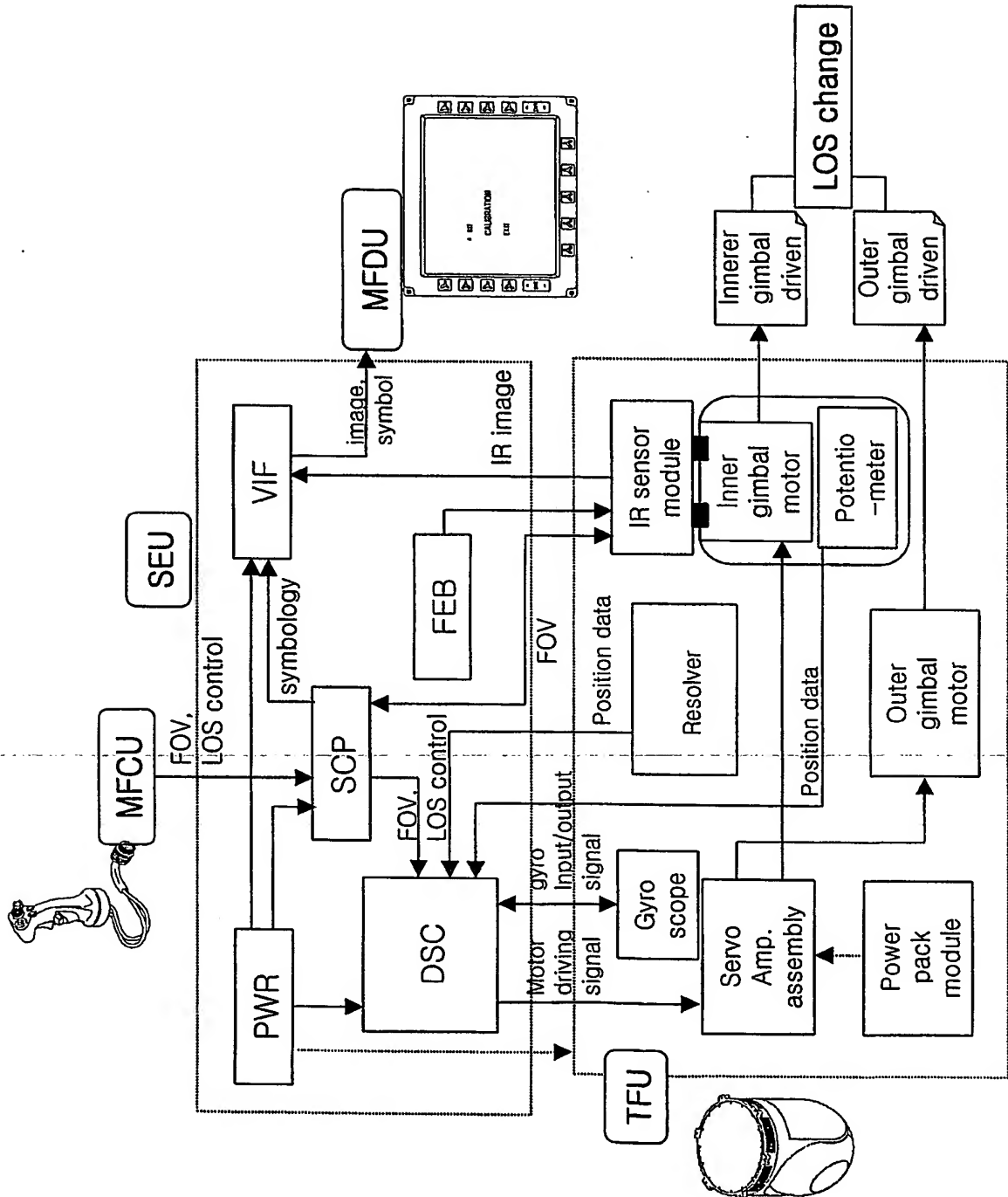
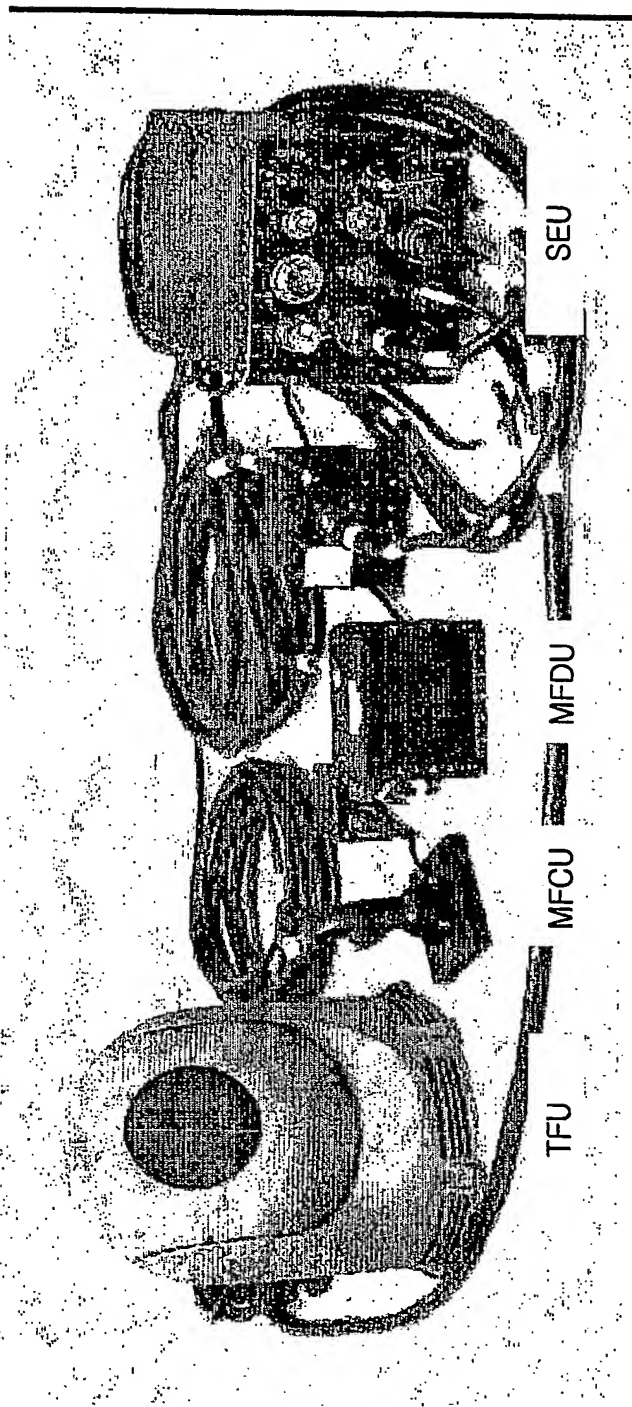


Fig. 6

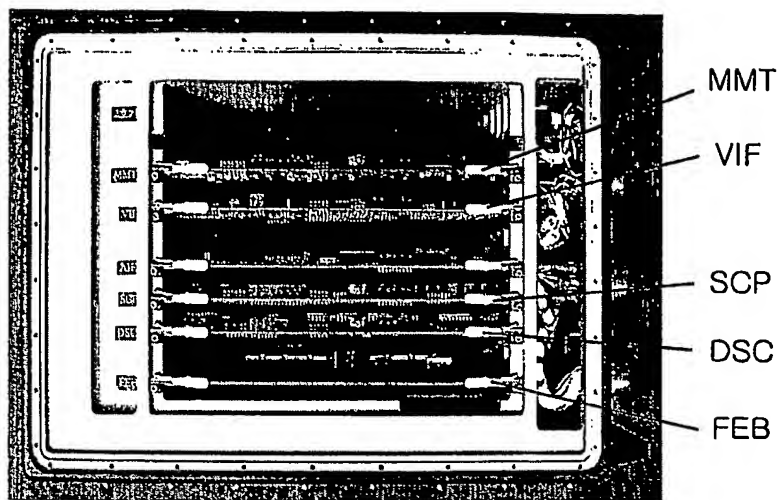


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Fig. 7

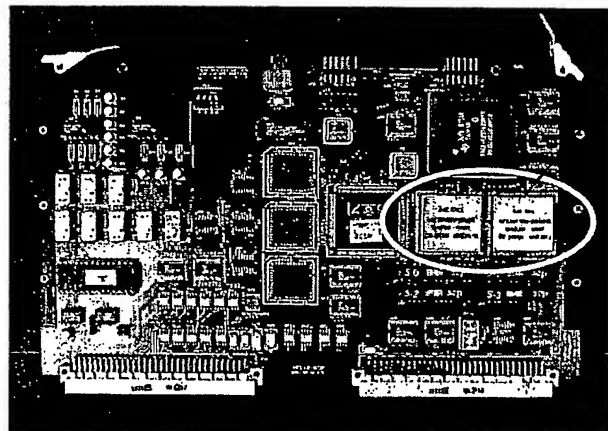


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Fig. 8

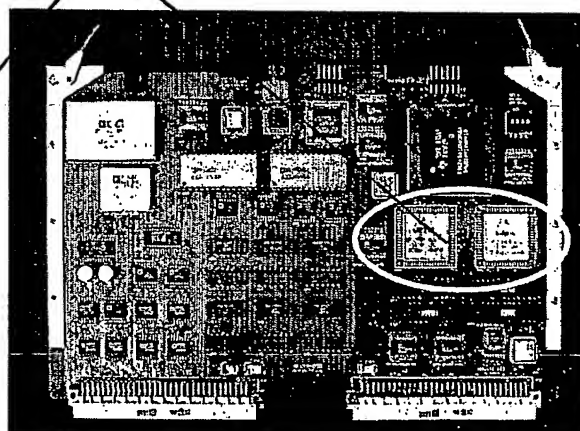


(a) SEU

flash memory  
(EPROM)



(b) SCP

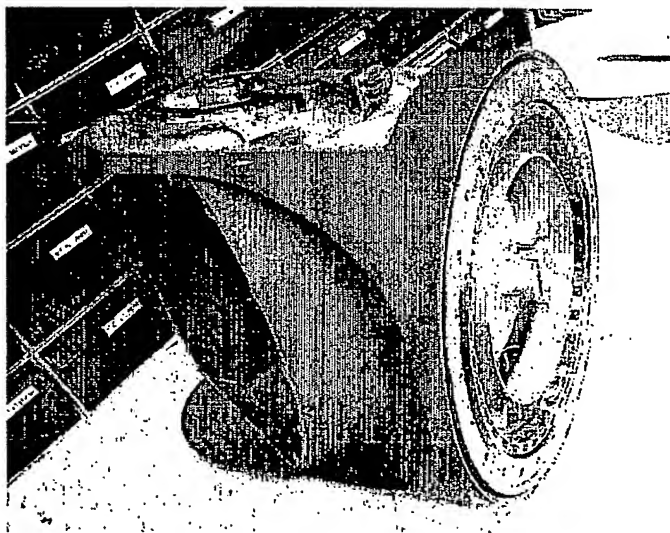
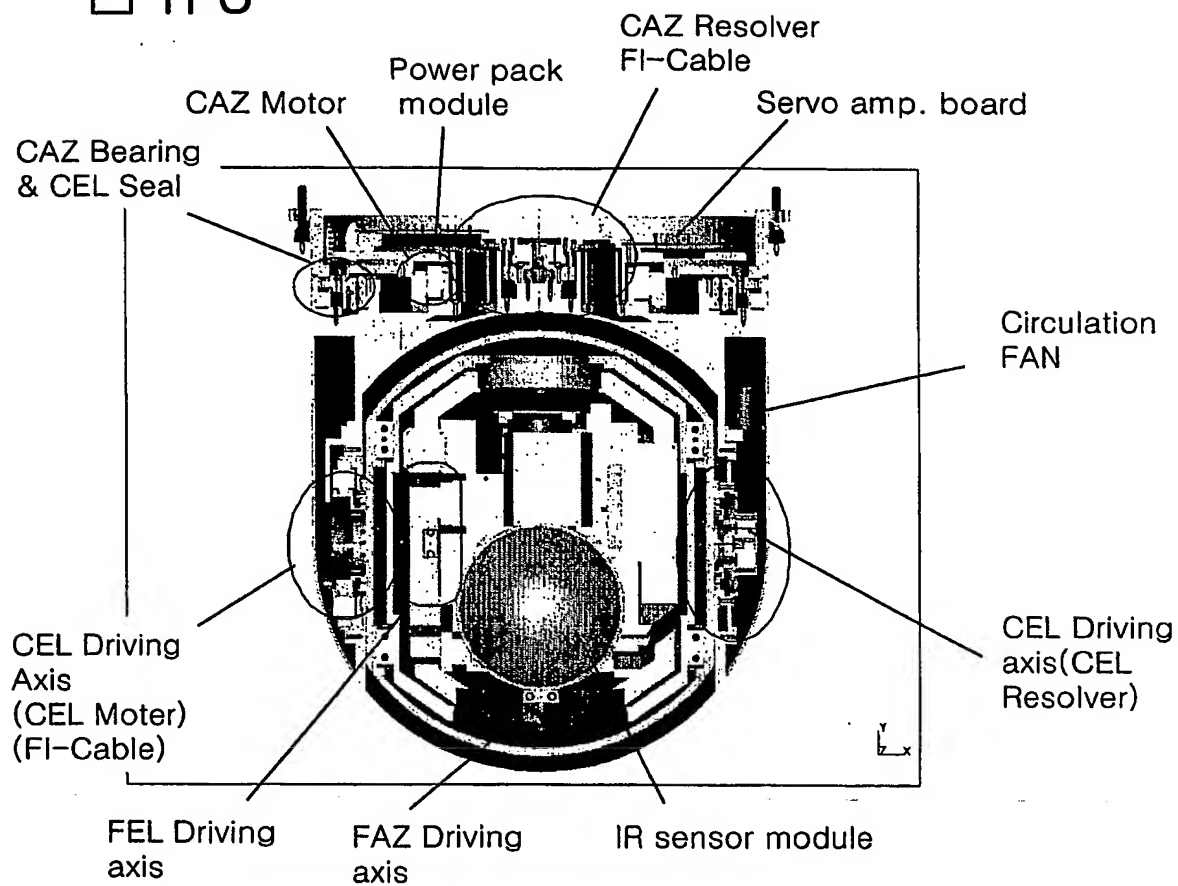


(c) DSC



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Fig. 9

□ TFU



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Fig. 10A

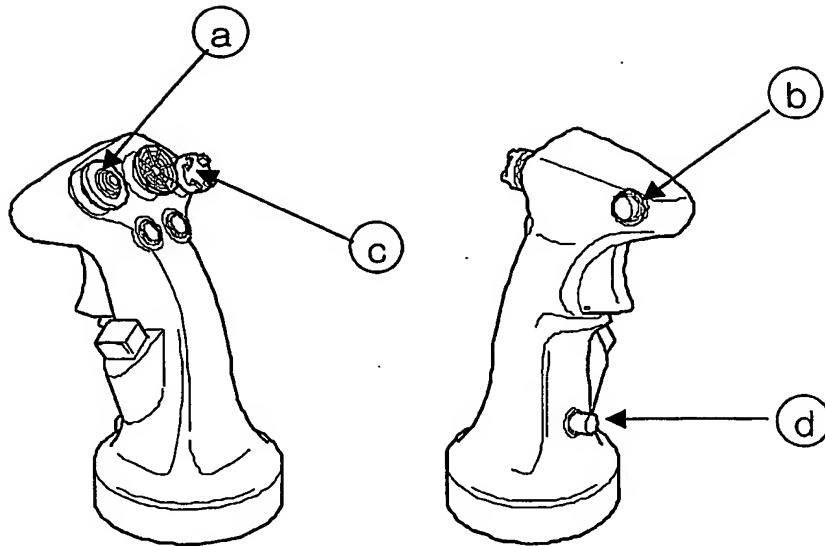
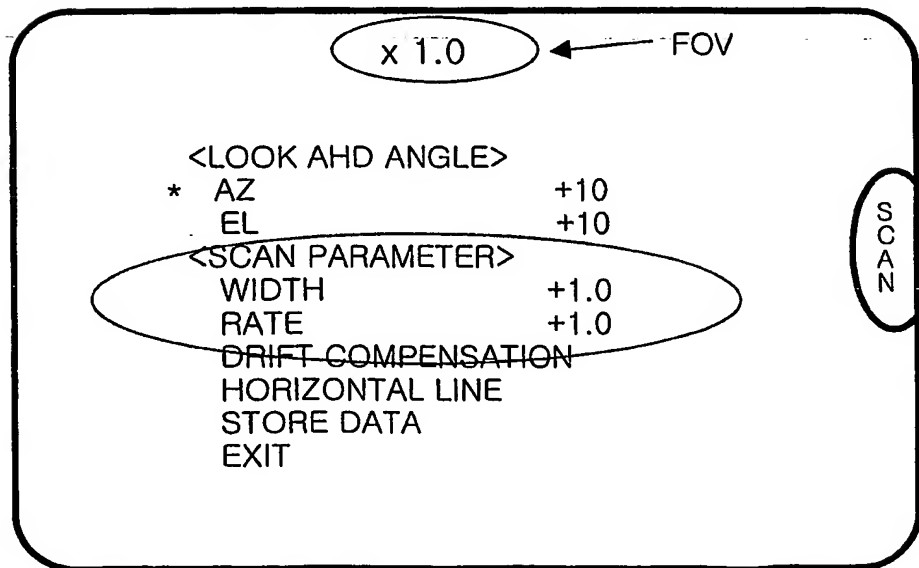
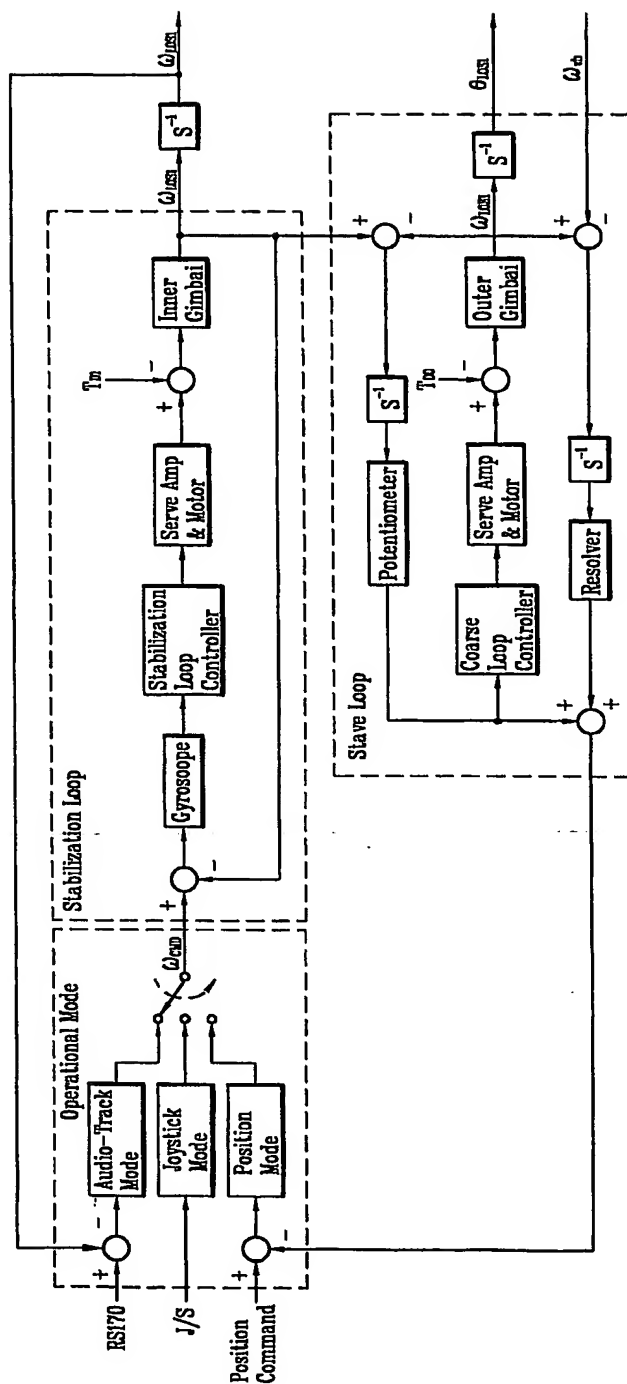


Fig. 10B



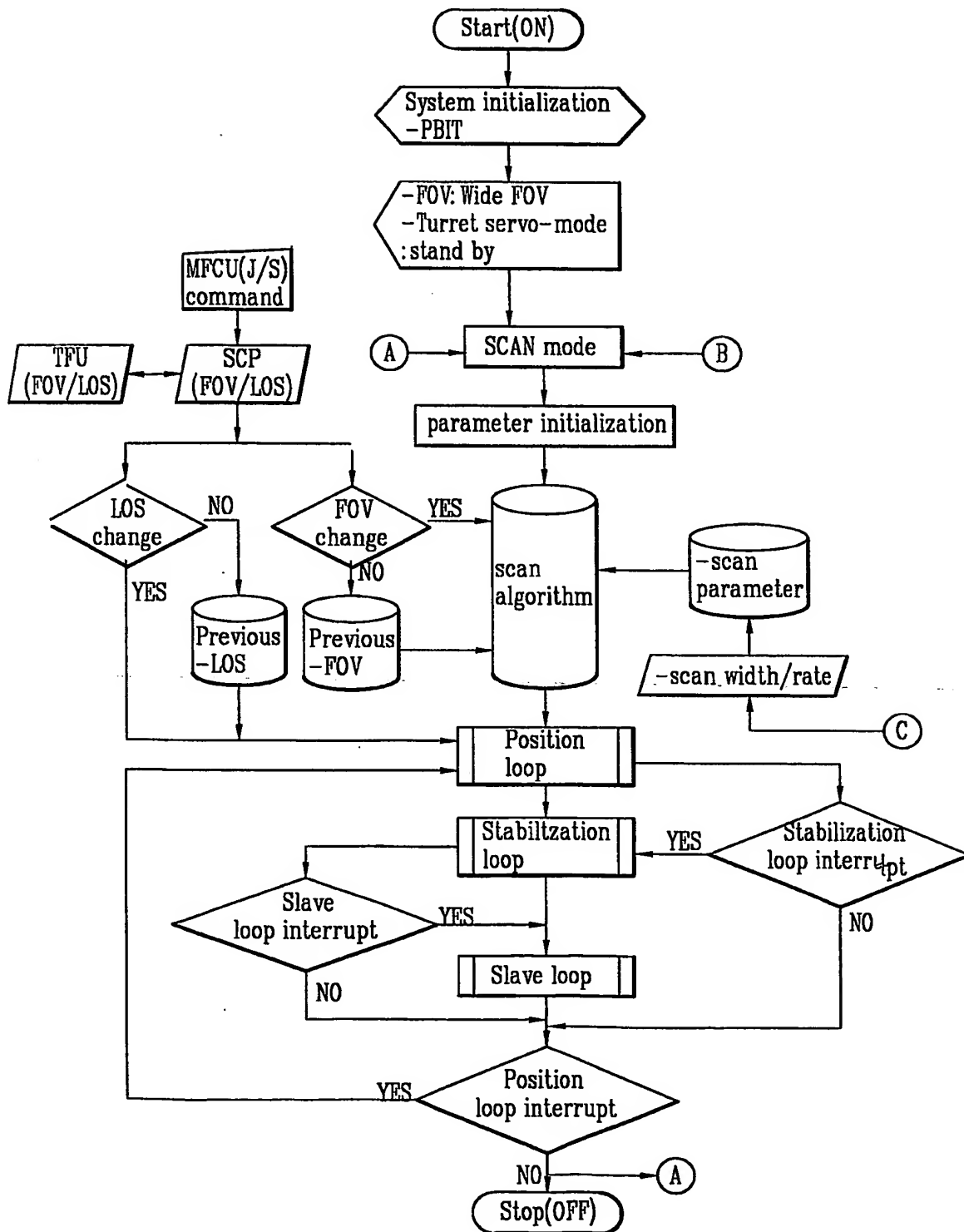
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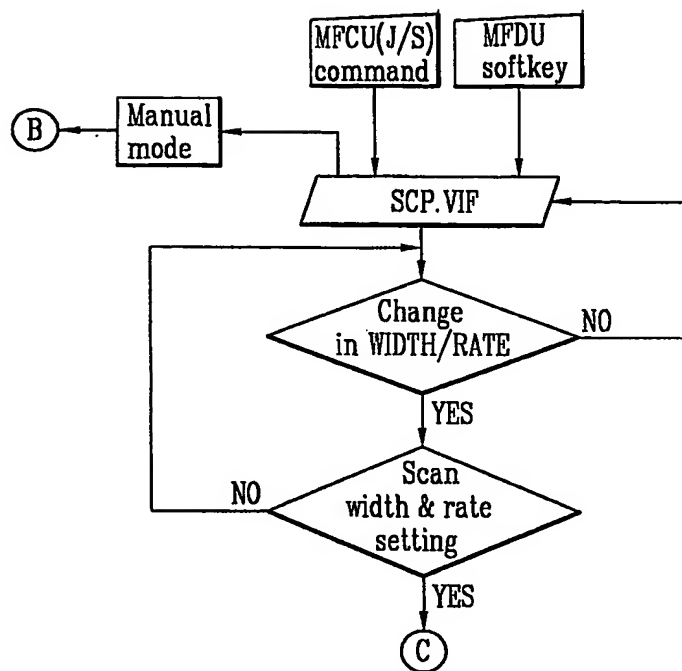
FIG. 11



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FIG. 12A



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FIG. 12B

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Fig. 13A

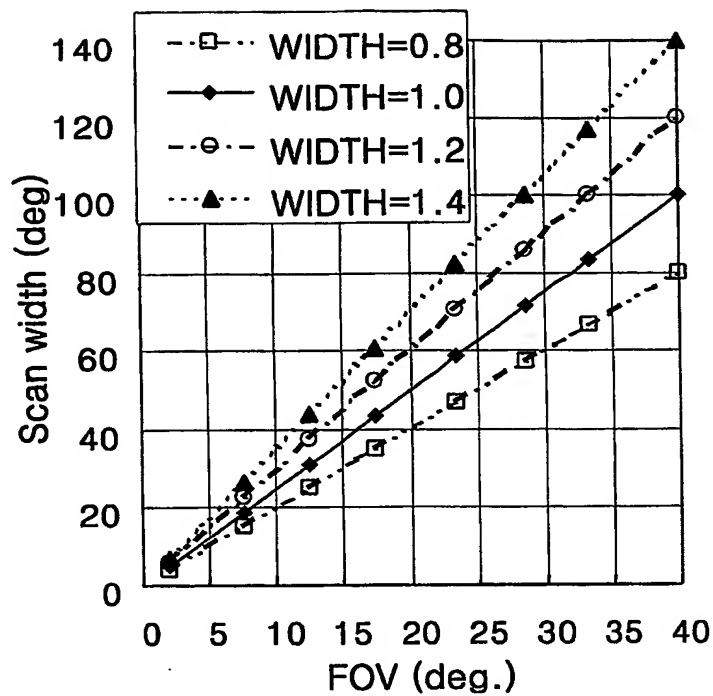
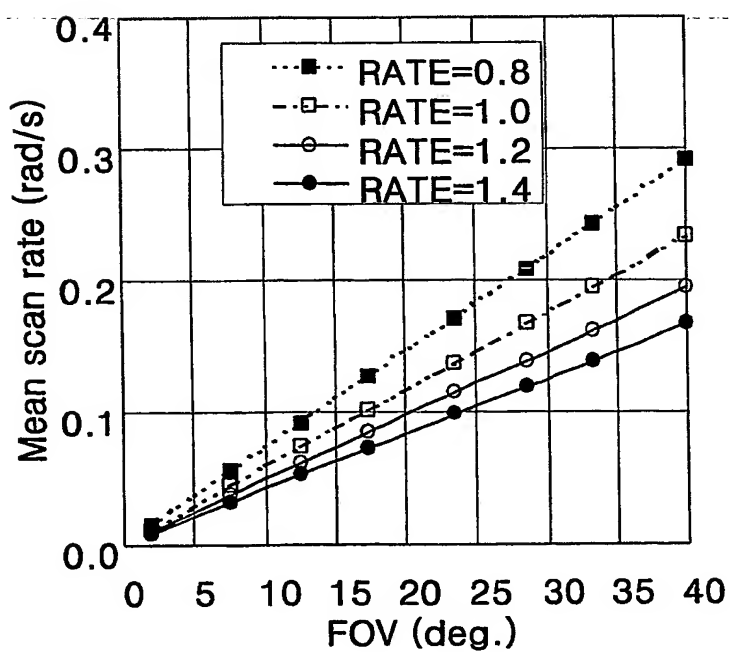


Fig. 13B



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Fig. 14A

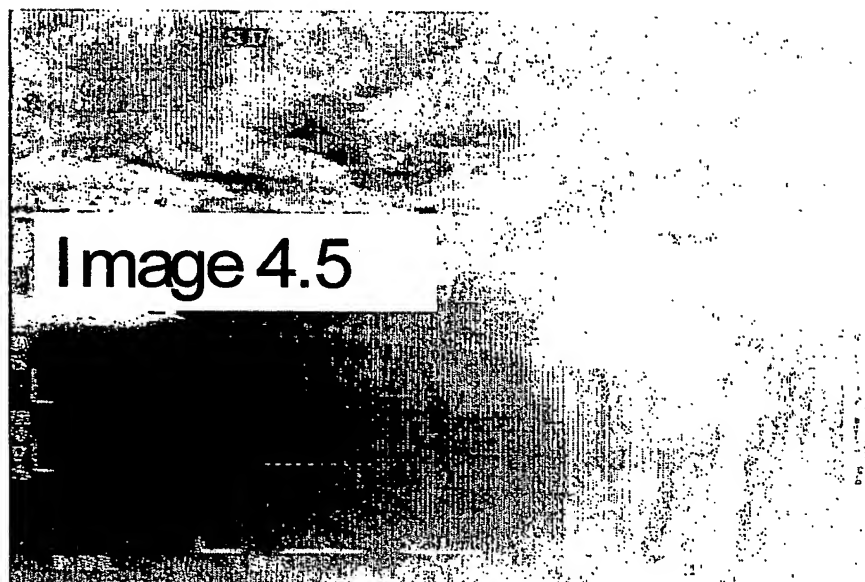
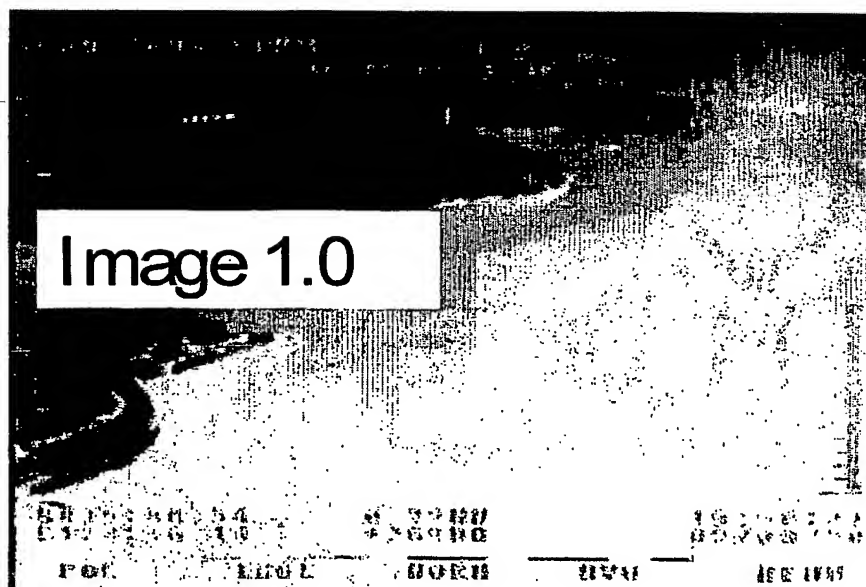


Fig. 14B



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